IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant(s): SUZUKI et al.

Atty. Dkt.: 1-85

Serial No.: Unknown

Group Art Unit:

Filed: Concurrently herewith

Assistant Commissioner for Patents

Examiner:

Title: SEMICONDUCTOR DEVICE AND

METHOD FOR MANUFACTURING

THE SAME

Date: September 29, 2000

Washington, D.C. 20231

INFORMATION DISCLOSURE STATEMENT

Sir:

Pursuant to 37 C.F.R. §1.56, the references listed on the attached Form PTO-1449 are being submitted for consideration by the Examiner without any admission that they constitute statutory prior art, or without any admission that they contain subject matter that anticipates the invention or renders the invention obvious to a person of ordinary skill in the art.

The Examiner is requested to initial the attached PTO Form-1449 and to return a copy of same to the undersigned attorney as proof that the listed references have been considered and made of record.

Respectfully submitted,

David G. Posz

Reg. No. 37,70

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